

MADEMA					
TRANSMITTAL FORM		Application No.	10/748,384		
		Filing Date	December 29, 2003		
(to be used for all correspondence after initial filing)		First Named Inventor	Tatiana Shpeisman		
		Art Unit			
		Examiner Name			
Total Number of Pages in This Submissi	on 6	Attorney Docket Number	42P17Z51		
* ENCLO	SURES (chec	k all that apply)			
Fee Transmittel Form	Drawing(a)		After Allowance Communication to TC		
Fee Atlached	Licensing-	related Papers	Appeal Communication to Board of Appeals and Interferences		
Amendment / Response	Petition		Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)		
After Final Affidavits/declaration(s)	Petition to (Provisional	Convert a Application	Proprietary Information		
Extension of Time Request	Power of Attorney, Revocation Change of Correspondence Address		Status Letter		
Express Abandonment Request	Terminal Disclaimer		Other Endosure(s) (please Identify below):		
Information Disclosure Statement	Request for Refund		- First Class Certificate of Mailing; - return recipp postuard; and		
PTO/SB/08 Cartified Copy of Priority	CD, Number	``	- copies of 4 of the cited references and a copy of the PCT Search Report		
Certified Copy of Priority Document(s)		cape Table on CD			
Response to Miseing Parts/ Incomplete Application	Remerks				
Basic Filling Fee Declaration/POA					
Response to Missing Perts under 37 CFR 1.52 or 1.53					
SIGNATURI	E OF APPLICAN	IT, ATTORNEY, OR AC	BENT		
Firm Libby H. Hopc, or INTEL CORPOR	•	74			
Signature Supplied Signature					
Date 2.7-66					
		ING/TRANSMISSION			
I hereby cartify that this correspondence is be sufficient postage as first class mail in an env 22313-1450.	ring deposited with relope addressed	n the United States Posta) : to: Commissioner for Pater	Service on the date shown below with tta, P.O. Box 1450, Alexandria, VA		
Typed or printed name Krista Math	ieson				
Signature Most	Mathie	son i	Date 3.7.06		



Docket No.: 42P17251

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

TATIANA SHPEISMAN, ET AL

Application No.: 10/748,384

Filed: December 29, 2003

For: DATA LAYOUT MECHANISM TO

REDUCE HARDWARE RESOURCE

CONFLICTS

Art Group:

Examiner:

STATUS INQUIRY

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

The above-captioned application, an amendment thereto, or an Appeal Brief was filed more than 18 months ago and no substantive Office Action has issued to date. Please provide the current status of the application and an indication of when the next Action is expected to issue.

Respectfully submitted,

INTEL CORPORATION

Date: 2-7-06

Libby H. (Hope, Reg. No. 46,774

12400 Wilshire Boulevard, 7th Floor Los Angeles, CA 90025 CERTIFICATE OF MAILING/TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA

Printe Markingon

22313-1450

Date

Docket No.: 42P17251

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

TATIANA SHPEISMAN, ET AL.

Application No.: 10/748,384

Filed: December 29, 2003

For: DATA LAYOUT MECHANISM TO

REDUCE HARDWARE RESOURCE

CONFLICTS



Art Group:

Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

3

The references were cited in a Search Report dated November 17, 2005 (copy enclosed herewith) in a counterpart PCT application.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 50-0221.

Respectfully submitted,

Date: 2.7.06

Libby H. Hope, Reg. No. 46,774

12400 Wilshire Boulevard, 7th Floor Los Angeles, CA 90025 Telephone:

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Krista Mathieson

Date

	•			4
	FEB	1	0	2006

2

Substitute for form 1449AF 10	. K
INFORMATION DISCLOSI	JRE
STATEMENT BY APPLICA	INA

(use as many sheets as necessary)

Sheet 1 of

Complete if Known				
Application Number	10/748,384			
Filing Date	December 29, 2003			
First Named Inventor	Tatiana Shpeisman			
Art Unit				
Examiner Name				
Attorney Docket Number	42P17251			

			U.S. PATE	NT DOCUMENTS	
		Document Number	Publication Date	Alama of Detector or Applicant	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
Examiner Initials*	Cite No.1	Number - Kind Code ² (if known)	Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	
		US-2001/052106	12-13-2001	Wuytack Sven, et al.	
		US-			
		US-		_	
		US-			
		US-			
		US-			
	ļ	US-			
		US-			
-		US-			
		US-			
		US-			
		US-			

	FOREIGN PATENT DOCUMENTS						
Examiner Initials* Cite No. Country Code' - Number' - Kind Code' (if known)		Foreign Patent Document	D. Minerine Date		Pages, Columns, Lines,	Т	
		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear			
		PCT/US2004/043238	11-17-2005	Intel Corporation (Search Report)		<u> </u>	
						-	
						╀	
						╁	
	ļ					\vdash	
	<u> </u>					+	
						T	
						T	
	+					Τ	

Examiner	Date
	Considered
Signature	Constacted

^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

^{&#}x27;Applicant's unique citation designation number (optional). 'See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 'Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 'For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. 'Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. 'Applicant is to place a check mark here if English language Translation is attached.

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known		
			CUDE	Application Number	10/748,384	
			SUKE	Filing Date	December 29, 2003	
			CANT	First Named Inventor	Tatiana Shpeisman_	
				Art Unit		
				Examiner Name		
Sheet	2	of	2	Attorney Docket Number	42P17251	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	_	KO M-Y, BHATTACHARYYA S S: "Partitioning for DSP Software Synthesis", Software and Compilers for Embedded Systems: 7th International Workshop, Scopes 2003, Vienaa, Sept. 24-26, 2003. Proceedings (Springer LNCS 2826), pgs. 344-358, XP002349106.	
		YU HEN HU (Editor): "Programmable Digitial Signal Processors", 2001 Marcel Dekker, XP002352402, ISBN: 0-8247-0647-1, Chap. 8 - S S Bhattacharyya: Hardware/Software Coysnthesis of DSP Systems (pgs 333-378).	
		BHATTACHARYYA S S ET AL: "Software Synthesis and Code Generation for Signal Processing Systems" IEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, vol. 47, no. 9, Sept 2000, pgs 849-875, XP002352333.	
		SAFARI ET AL: "Testability improvement during high-level synthesis", 12th Asian Test Symposium, 2003. ATS 2003. Nov. 16-19, 2003, Piscataway, NJ, USA, IEEE, Nov. 19, 2003, page 505, XP010672538.	

A STATE OF THE PARTY OF THE PAR	
Examiner	Date
Signature	Considered

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

'Applicant's unique citation designation number. 'Applicant is to place a check mark here if English language Translation is attached.